

ABSTRACT

The invention concerns an amplifier comprising a specific number of N active elements (11 to 1N) coupled in parallel to a load impedance (2) via an adaptive device (3) including at least a specific number of N referenced susceptance compensating circuits (41 to 41N). The susceptance compensating circuits (41 to 41N) are connected respectively to the outputs of the N Active elements (11 to 1N) to compensate the output susceptance of the active elements to a conductance combining and adapting circuit (5) having N inputs connected respectively to the outputs of the N susceptance compensation circuits and an output connected to the load impedance (2) of the amplifier. The invention is applicable to microwave amplifiers with high output power dynamics.